



AN118 – EVD1000/1500 Color Space Conversion: RGB to YCrCb, SMPTE 240

INTRODUCTION

The EVD1000/1500 has a very flexible digital I/O structure, and in-circuit applications can be configured in a variety of fashions, utilizing different video data formats, levels of user control, etc. In this series of Application Notes, a number of examples of color space conversion are presented, each of which includes a complete set of conversion matrix coefficient register values as required for operation in that particular example mode. For more complete and general application information, please refer to the EVD1000/1500 Data Sheet. The present Application Note describes the method of conversion from input RGB to the internal YCrCb space, e.g., as per SMPTE 240.

Color Space Conversion

All internal processing in the EVD1000/1500 is performed in YCrCb mode. Additionally Y values are assumed to be in the range of {64...940}. Cr and Cb values are assumed to be in the range of {64...960}. If RGB data is sent to the EVD1000/1500 an RGB to YCrCb conversion must be performed on the input data before it is processed. If RGB data is required to be output from the chip then an YCrCb to RGB conversion is required to be performed on the processed data before it is sent from the chip. Two independent converters perform the conversions. The coefficients in each converter may be programmed to allow for different relationships between RGB and YCrCb as required for different video standards.

RGB to YCrCb Conversion

Input color conversion from RGB to YCrCb is enabled whenever the input mode of the EVD1000/1500 is set to RGB. See the Data Sheet for details.

SMPTE 240

From the earlier Application Note on SMPTE 240 output conversion, the fundamental relationship between Y, Cr, Cb, R, G, and B is as follows:

$$(161) \quad Y = .212 \times R + .701 \times G + .087 \times B$$

$$(162) \quad Cr = .500 \times R - .44479 \times G - .05520 \times B$$

$$(163) \quad Cb = -.11610 \times R - .38389 \times G + .500 \times B$$

Where Y,R,G,B {0...1} and Cr,Cb {-0.5...+0.5}



For 10-bit digital implementation Y_D is in the range {64...940}, Cr_D , Cb_D are in the range {64...960} and R_D , G_D , B_D are in the range {0...1023}. The above three equations are modified as follows:

$$(164) \quad Y_D = \left(\frac{940 - 64}{1023} \right) \times (.212 \times R_D + .701 \times G_D + .087 \times B_D) + 64 + .5$$

$$(165) \quad Y_D = \frac{186 \times R_D + 615 \times G_D + 76B_D + 66048}{1024}$$

$$(166) \quad Cr_D = \left(\frac{960 - 64}{1023} \right) \times (.500 \times R_D - .44479 \times G_D - .05520 \times B_D) + 512 + .5$$

$$(167) \quad Cr_D = \frac{448 \times R_D - 399 \times G_D - 50 \times B_D + 524,800}{1024}$$

$$(168) \quad Cb_D = \left(\frac{960 - 64}{1023} \right) \times (-.11610 \times R_D - .38389 \times G_D - .500 \times B_D) + 512 + .5$$

$$(169) \quad Cb_D = \frac{-104 \times R_D - 344 \times G_D + 448 \times B_D + 524,800}{1024}$$

The EVD1000/1500 converts RGB to YCrCb using the following equations:

$$(170) \quad Y_D = \frac{RGB2YCRCB_KYR \times R_D + RGB2YCRCB_KYG \times G_D + RGB2YCRCB_KYB \times B_D + RGB2YCRCB_KYOFF}{1024}$$

$$(171) \quad Cr_D = \frac{RGB2YCRCB_KCRR \times R_D + RGB2YCRCB_KCRG \times G_D + RGB2YCRCB_KCRB \times B_D + RGB2YCRCB_KCROFF}{1024}$$

$$(172) \quad Cb_D = \frac{RGB2YCRCB_KCBR \times R_D + RGB2YCRCB_KCBG \times G_D + RGB2YCRCB_KCBB \times B_D + RGB2YCRCB_KCBOFF}{1024}$$

The following table shows the values that must be written to the registers in the RGB to YCrCb Converter to perform SMPTE 240 RGB to YCrCb Conversion. The values in the top line of each row are signed full width register values (24-bits for registers ending in OFF and 16-bits for other registers). The values in the bottom line of each row are 8-bit width register values. Each full width register is implemented by concatenating two or three 8-bit registers.



Register Assignments SMPTE 240 RGB to YCRCB Conversion (8-bit Registers)	Value (decimal)	Value (HEX)
RGB2YCRCB_KYR (RGB2YCRCB_KYR_HIGH:RGB2YCRCB_KYR_LOW)	186 (0:186)	00B8 (00:B8)
RGB2YCRCB_KYG (RGB2YCRCB_KYG_HIGH:RGB2YCRCB_KYG_LOW)	615 (2:103)	0267 (02:67)
RGB2YCRCB_KYB (RGB2YCRCB_KYB_HIGH:RGB2YCRCB_KYB_LOW)	76 (0:76)	004C (00:4C)
RGB2YCRCB_KYOFF RGB2YCRCB_KYOFF_HIGH:RGB2YCRCB_KYOFF_MID:RGB2YCRCB_KYOFF_LOW)	66048 (01:02:00)	010200 (01:02:00)
RGB2YCRCB_KCRR (RGB2YCRCB_KCRR_HIGH:RGB2YCRCB_KCRR_LOW)	448 (1:192)	01C0 (01:C0)
RGB2YCRCB_KCRG (RGB2YCRCB_KCRG_HIGH:RGB2YCRCB_KCRG_LOW)	-399 (254:113)	FE71 (FE:71)
RGB2YCRCB_KCRB (RGB2YCRCB_KCRB_HIGH:RGB2YCRCB_KCRB_LOW)	-50 (255:206)	FFCE (FF:CE)
RGB2YCRCB_KCROFF RGB2YCRCB_KCROFF_HIGH:RGB2YCRCB_KCROFF_MID:RGB2YCRCB_KCROFF_LOW)	524,800 (8:2:0)	080200 (08:02:00)
RGB2YCRCB_KCBR (RGB2YCRCB_KCBR_HIGH:RGB2YCRCB_KCBR_LOW)	-104 (255:152)	FF98 (FF:98)
RGB2YCRCB_KCBG (RGB2YCRCB_KCBG_HIGH:RGB2YCRCB_KCBG_LOW)	-344 (254:168)	FEA8 (FE:A8)
RGB2YCRCB_KCBB (RGB2YCRCB_KCBB_HIGH:RGB2YCRCB_KCBB_LOW)	448 (1:192)	01C0 (01:C0)
RGB2YCRCB_KCBOFF RGB2YCRCB_KCBOFF_HIGH:RGB2YCRCB_KCBOFF_MID:RGB2YCRCB_KCBOFF_LOW)	524,800 (8:2:0)	080200 (08:02:00)

For further questions or clarifications, contact your sales representative or the factory for additional support.

Enhanced Video Devices, Inc.
9830 Summers Ridge Road
San Diego, CA 92121
858-530-0100

www.enhancedvideodevices.com

Copyright © 2009 Enhanced Video Devices, Inc. (EVD). All rights reserved. The information contained herein is subject to change without notice in order to improve design and/or performance. EVD products are protected under numerous U.S. and foreign patents and pending applications, maskwork rights, and copyrights. EVD assumes no responsibility or liability for the use of any of its information, products, or services, and conveys no license or title under any patent, copyright, or mask work right to its products, unless otherwise expressly specified and agreed in writing. Furthermore, EVD's products are not designed for use as critical components in life support, life saving, critical control or safety applications where a malfunction or failure may reasonably be expected to result in significant injury or harm. The inclusion of EVD products in such applications implies that the manufacturer assumes all risk of such use and in doing so fully indemnifies EVD against all damages resulting from such application. Any applications that are described herein are for illustrative purposes only.